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Express Mail No.: ER 843 147 288 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Reese, et al.

Serial No.: 10/687,420

Filed: October 15, 2003

For: METHOD FOR IMPLEMENTING
ELECTRO-STATIC DISCHARGE
PROTECTION IN SILICON-ON-
INSULATOR DEVICES

Confirmation No: 7991

Group Art Unit: 2818

Examiner: Andy Huynh

Attorney Docket No.: 060889-0093

RESPONSE TO RESTRICTION REQUIREMENT MAILED DECEMBER 29, 2004

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement mailed December 29, 2004, in connection with the above-identified application, applicants elect Group I, claims 1-10 with traverse, for further prosecution. Applicants reserve the right to prosecute claims 11-20, in a continuation application.